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XA-9582
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yoshihiro IKEDA et al.

Appln. No.:

Filed: HERewith

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
MANUFACTURING METHOD THEREOF



* * *

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion
as to materiality or prior art effect, the documents listed
on the attached Form PTO-1449 are hereby cited.

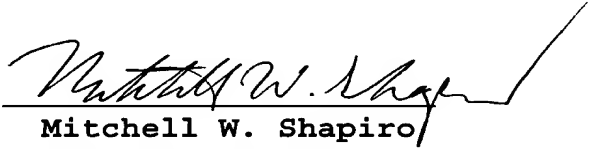
The documents on the attached List are cited in the
specification, on pages 1 and 2, and their relevance is
indicated therein.

Respectfully submitted,

MWS:sjk

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November 16, 2001

By:


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